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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/661,787	09/11/2003	Gregory N. Henderson	TRQ-12904	8358

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EXAMINER

TRAN, PABLO N

ART UNIT	PAPER NUMBER
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2685

DATE MAILED: 12/01/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No. 10/661,787	Applicant(s) HENDERSON ET AL.	
	Examiner Pablo N. Tran	Art Unit 2685	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 11 August 2005.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-36 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-36 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

2. Claims 1-7, 9, 11, 28-30, and 35-36 are rejected under 35 U.S.C. 102(e) as being anticipate by Lopez et al. (2003/0006845).

As per claim 1, 3, Lopez et al. disclose an automatic-bias amplifier circuit having an amplifier (fig. 2/no. 120) comprising an input, an output, and a signal path between and including the input and output of the amplifier, wherein the amplifier is coupled to a fixed-level DC voltage source; a feedback loop coupled between the signal path and a bias input of the amplifier, wherein the feedback loop comprises a power detector (fig. 2/no. 140) and a bias circuit (fig. 2/no. 150) , wherein the power detector comprises an input coupled to the signal path, and is operable for sampling a first signal on the signal path and for outputting to the feedback loop an analog voltage signal reflective of the power of the first signal, and wherein the bias circuit is on the feedback loop between the power detector and the bias input of the amplifier, and causes the amplifier to draw

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a quiescent current, from the fixed-level DC voltage source, that varies proportionally with the analog voltage signal [0010, 0026-0027].

As per claims 2, 4, 28-29, and 35-36, as stated above in claim 1, Lopez et al. disclose the power detector is coupled to the signal path at an internal node of the amplifier, said internal node being between, but exclusive of, the input and the output of the amplifier (see fig. 2).

As per claim 5, Lopez et al. disclose the power detector is coupled to the output of the amplifier (see fig. 2).

As per claims 6 and 30, Lopez et al. disclose the amplifier comprises a plurality of amplifier stages (fig. 2/no. 110, 120, 130) and the power detector is coupled for sampling between a pair of the amplifier stages.

As per claims 7, 9, and 11, Lopez et al. discloses such voltage shifting control circuitry (fig. 2/no. 170).

Claim Rejections - 35 USC § 103

3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

4. Claims 8, 10, 20-24, 27, and 31-32 are rejected under 35 U.S.C. 103(a) as being unpatentable over Lopez et al. (2003/0006845) in view of Persico et al. (2002/0146993).

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As per claims 8 and 10, the modified apparatus of Lopez et al. does not explicitly disclose such circuitry are disposed on a single IC. However, such is notoriously well known in the art, as suggested by Persico et al. [0089]. However, such circuitry disposition is obvious to one of ordinary skill in the art in order to provide such flexibility to install/replace depending upon the specific implementation/arrangement of the communication system. Furthermore, the modified apparatus of Lopez et al. does not specifically the voltage shifting circuitry is not on the IC. However, such circuitry disposition is obvious to one of ordinary skill in the art in order to provide such flexibility to install/replace the appropriated voltage shifting control circuitry depending upon the specific implementation of the bias circuit.

As per claims 20, 24, and 27, as stated above in claim 1, the modified apparatus of Lopez et al. further disclose a base band processor (fig. 1/no. 110) and antenna [0026].

As per claim 21, the modified apparatus of Lopez et al. further discloses the claimed limitation as stated above in claim 2.

As per claim 22, the modified apparatus of Lopez et al. further discloses the claimed limitation as stated above in claim 6.

As per claims 23 and 31-32, the modified apparatus of Lopez et al. further discloses a pre amplifier (see Lopez et al., fig. 1/no. 110).

5. Claims 12-15, 16, and 33-34 are rejected under 35 U.S.C. 103(a) as being unpatentable over Lopez et al. (2003/0006845) in view of King (6,137,366).

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As per claims 12-13, 16, and 33, Lopez et al. disclosed the claimed limitations as stated above in claim 1, but silent about a current mirror. However, such bias circuitry configuration is well known, as suggested by King (fig. 5). Therefore, it would have been obvious to one of ordinary skill in the art to utilize such bias circuitry configuration of Ichihara in place of the bias circuitry of Lopez et al. in order to provide a transmission power control effectively and to lengthen the talk time of the portable telephone.

As per claims 14 and 34, the modified apparatus of Lopez et al. further discloses a constant current portion and a continuously varying portion (see King, col. 5/ln. 15-col. 6/ln. 9).

As per claims 15, the modified apparatus of Lopez et al. is silent about such non-unity current ratio. However, such is notoriously well known in the art that the examiner takes Official Notice of such. Therefore, it would have been obvious to one of ordinary skill in the art to utilize such current mirror ratio circuitry to the modified circuitry of Ichihara in order to effectively provided a proper current attenuation to the bias circuit

6. Claims 17-18 and 25-26 are rejected under 35 U.S.C. 103(a) as being unpatentable over the modified apparatus of Lopez et al. in view of Persico et al. (2002/0146993).

As per claims 17-18, the modified apparatus of Lopez et al. disclose such voltage shifting control circuitry (fig. 2/no. 170) but does not explicitly discloses such circuitry are disposed on a single IC. However, such is notoriously well known in the art, as suggested by Persico et al. [0089]. However, such circuitry disposition is obvious to

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one of ordinary skill in the art in order to provide such flexibility to install/replace depending upon the specific implementation/arrangement of the communication system. Furthermore, the modified apparatus of Lopez et al. does not specifically the voltage shifting circuitry is not on the IC. However, such circuitry disposition is obvious to one of ordinary skill in the art in order to provide such flexibility to install/replace the appropriated voltage shifting control circuitry depending upon the specific implementation of the bias circuit.

As per claim 19, the modified apparatus of Lopez et al. further discloses the claimed limitation as stated above in claim 14.

As per claim 25, the modified apparatus of Lopez et al. further discloses the claimed limitation as stated above in claim 12.

As per claim 26, the modified apparatus of Lopez et al. further discloses the claimed limitation as stated above in claim 14.

Conclusion

7. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Pablo Tran whose telephone number is (571)272-7898. The examiner normal hours are 9:30 -5:00 (Monday-Friday). If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Edward Urban, can be reached at (571)272-7899. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

8. Information regarding the status of an application may be obtained from the

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Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR.

Status information for unpublished applications is available through Private PAIR only.

For more information about the PAIR system, see <http://pair-directauspto.gov>. Should

you have questions on access to the Private PAIR system, contact the Electronic

Business Center (EBC) at 866-217-9197 (toll-free).

November 25, 2005

PABLO N. TRAN
PRIMARY EXAMINER



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